INVESTIGATION OF DYNAMIC MODELING OF DVR

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Abstract- The dynamic voltage restorer (DVR) is one of the flexible ac transmission system (FACTS) is vastly applied in power systems. The direct three-phase to single-phase device can be used effectively for mitigation of voltage sags and swells. This paper provides comprehensive development procedures and final forms of mathematical models of a new structure of DVR based on direct three-phase to single-phase converter. Through the presented modeling, the transient and steady states responses are analyzed. The validity of the presented modeling for DVR has been verified by simulation results using MATLAB software.

Keywords: DVR, FACTS, Dynamic Modeling, Steady State Modeling, Transient State Modeling.

I. INTRODUCTION

Power quality has always been important for customer, but with increasing number of electronic loads and sensitive controllers to power quality, the subjects have attracted renewed interest in recent time [1-4]. DVR is a power electronic device to magnify the voltage distortion in distribution systems. The basic operation of DVR is to inject a voltage of required magnitude, phase angle, and frequency in series with a distribution feeder to maintain the desired amplitude and waveform for load voltage even when voltage is unbalanced or distorted [5].

Many topologies and control methods have been presented for DVR. One of the best structures is based on direct three-phase to single-phase. Figure 1 shows the schematic diagram of the presented topology for DVR [6]. As shown in Figure 1, the presented DVR consist of two stages, converter side and injection side in each phase. This topology has more advance rather than conventional topologies. The presented topology is properly able to compensate unbalanced voltage sags and swells according to application of three independent three-phase to single-phase converters. Moreover, this structure does not need to energy-storage elements.

In this paper, it is tried to investigate and present a dynamic model for this type of DVR structure. First, a brief review is given for recommended DVR in [6]. Then, the mathematical modeling of this system is presented. Finally, the validity of presented theories is confirmed by simulation results.

II. REVIEW ON RECOMMENDED DVR [6]

As shown in Figure 1, in each phase of the DVR, a three-phase to single-phase direct converter, an filter \((L_k, C_k)\) for \(k = 1,2,3\), a bypass switch \((S_{b,k})\) for \(i = a,b,c\), a resistance \((R_k)\) for \(k = 1,2,3\), and an injection transformer have been used. Also, each three-phase to single-phase direct converters consist of six power switches. Considering Figure 1 and assuming sinusoidal waveforms, these following equations can be obtained for phase ‘a’ of converter 1, and phase ‘a’ of injection voltage [6]:

\[
\begin{align*}
    v_{i,1}(t) &= v_{i,1,m} \sin \omega t \\
    v_{a}(t) &= v_{i,a,m} \sin(\omega t + \phi_i)
\end{align*}
\]

where \(\phi_i\) is defined as follows [6]:

\[
\phi_i = \begin{cases} 
0^\circ, & \text{for sag conditions} \\
180^\circ, & \text{for swell conditions}
\end{cases}
\]

The relation between \(v_{d,k}(t), v_{i,k,h}(t)\), and \(v_{j,k}(t)\) (for \(k = 1,2,3\), \(h = a,b,c\), \(i = a,b,c\)) can be expressed by [6]:

\[
\begin{align*}
    v_{d,k}(t) &= m_k v_{i,k,h}(t) \quad \text{for} \quad k = 1,2,3 \quad \text{and} \quad h = a,b,c \\
    v_{j,k}(t) &= m_i v_{d,k}(t) \quad \text{for} \quad k = 1,2,3 \quad \text{and} \quad i = a,b,c
\end{align*}
\]
III. MATHEMATICAL MODELING

A. Converter Side Modeling

The operation of input control parameters of the presented DVR in each converter and their mutually phase are investigated to evaluate its dynamic model. In order to achieve this aim, converter 1 and its mutually injection phase, ‘a’, are considered. Here, it is assumed that \( C_1 \), is divided into two equal parts and its middle point is named as \( n \). In addition, \( \xi_{1,a} \) and \( \xi'_{1,a} \), switches are bidirectional ones with \( r_s \) resistance, which can be considered as the conduction losses of the switches and can be applied in losses analysis. In the following, it is assumed that \( r_{1,a} = R_1 + r_{s,a} \).

The control method of direct converters in presented DVR is based on pulse width modulation (PWM) technique. Applying PWM technique for converter 1, the following is valid [7-9]:

\[
S_{1,a} + S'_{1,a} = 1
\]  

By applying the kirchoff’s voltage law in Figure 2, the following is obtained:

\[
(L_1 + L_{1,a}) \frac{d_i_{1,a}}{dt} + r_{1,a}i_{1,a} = v_{1,1,a} - v_{1,a}
\]  

Considering Figure 2, the following relation can be expressed:

\[
v_{1,a} = v_{FH} + v_{ln}
\]

where, \( v_{FH} \) can be calculated as follows:

\[
v_{FH} = (i_{1,a}r_s + v_{dc,1})S_{1,a} + i_{1,a}r_sS'_{1,a}
\]

Considering (8) and (9), and assuming \( R_{1,a} = r_s + r_{1,a} \) and \( L_{1,a} = L_1 + L_{1,a} \), the following equation can be obtained:

\[
L_{1,a} \frac{di_{1,a}}{dt} = -R_{1,a}i_{1,a} - (v_{dc,1}S_{1,a} + v_{ln}) + v_{1,1,a}
\]

According to Figure 2, the parameter \( v_{ln} \) can be calculated as follows [10]:

\[
v_{ln} = \left( \frac{v_{dc,1}}{3} \right) \sum_{h=a,b,c} S_{1,h}
\]

If \( S_{1,a} \) is expanded by Fourier discrete series [10], the following is valid:

\[
\bar{d}_{1,a} = \frac{m_{dc,1}}{2} \cos(\omega t + \delta_1) + \frac{1}{2}
\]

Considering (11), \( v_{1,a} \) can be expressed as follows:

\[
v_{1,a} = A_1 \cos \delta_1 = \frac{m_{dc,1}}{2} \cos(\omega t + \delta_1)
\]

Finally, considering the relation obtained for phase ‘a’ of converter 1, and assuming same power switches in converter 1, the following equation can be obtained:

\[
L_{1,a} \frac{di_{1,a}}{dt} = -R_{1,a}i_{1,a} - A_1 \cos \delta_1 + v_{1,1,a}
\]

The obtained mathematical model for phase ‘a’ can be acceptable for phase ‘b’ and ‘c’ with 120° and 240° phase difference, respectively. In addition, the obtained equations for converter 1 are acceptable for converters 2 and 3.

B. Injection Side Modeling

From (11) and (12), the average positive half switching function of \( S_{b,a} \) can be calculated as follows:

\[
\bar{d}_{1,a} = \frac{m_{dc,1}}{2} \cos(\omega t + \delta_1) + \frac{1}{2}
\]

Considering (13), (14), and (15), the injected voltage of phase ‘a’ of is given by:

\[
L_{1,a} \frac{di_{1,a}}{dt} = -R_{1,a}i_{1,a} - A_1' \cos \delta_a' + v_{1,a}'
\]

The obtained equation for phase ‘a’ is acceptable for phase ‘b’ and ‘c’ with 120° and 240° phase difference, respectively.

C. Filter Modeling

In order to dynamic modeling of filter capacitor in converter 1 and phase ‘a’, the following equation can be considered:

\[
\frac{dv_{dc,1}}{dt} = \frac{1}{C_1} i_{dc,1}
\]

where \( i_{dc,1} \) is defined as follows:

\[
i_{dc,1} = i_{dc} - \bar{i}_{dc} = \sum_{h=a,b,c} i_{h,dc} \bar{d}_{h} - \bar{i}_{h,dc} \bar{d}_{1,a}
\]

The obtained mathematical modeling for converter 1 and phase ‘a’ are acceptable for converter 2 and phase ‘b’, and converter 3 and phase ‘c’ with suitable phase difference, respectively.

IV. STATE SPACE EQUATIONS DERIVATION

Considering same switches in presented DVR \( R_{k,h} = R \), \( R'_{k,h} = R' \), \( L_{k,h} = L \), and \( L'_{k,h} = L' \) and from (14), (16) and (17), a state space equation can be defined:

\[
X = AX + BU
\]

where,

\[
X = [i_{1,a}, i_{b,c}, i_{2,a}, i_{2,b}, i_{2,c}, i_{3,a}, i_{3,b}, i_{3,c}]
\]

\[
U = [v_{1,1,a}, v_{1,1,b}, v_{1,1,c}, v_{1,2,a}, v_{1,2,b}, v_{1,2,c}, v_{1,3,a}, v_{1,3,b}, v_{1,3,c}]
\]

The non-zero arrays of \( A \) and \( B \) values have been presented in Appendix.
V. STEADY STATE MODELING

Neglecting DVR losses and assuming that $v_{dc,k}(t)$ (for $k = 1,2,3$) is constant, and applying KVL in investigated DVR, following equation can be obtained:

$$V_{k,h} = jX_{k,h}I_{k,h} + V_{i,h} \quad \text{(for } h = a,b,c; \quad k = 1,2,3) \quad \text{(22)}$$

Thus, according to (22), the DVR steady state model is obtained as follows:

$$\begin{bmatrix}
-TZ_1 & 0 & -Z_2 & -Z_3 \\
-Z_1 & -TZ_2 & 0 & -Z_3 \\
-Z_2 & -Z_1 & -TZ_3 & 0 \\
-Z_3 & -Z_2 & -Z_1 & -TZ_3
\end{bmatrix}
\begin{bmatrix}
\Delta i_{a} \\
\Delta i_{b} \\
\Delta i_{c}
\end{bmatrix}
= \begin{bmatrix}
\Delta V_{a} \\
\Delta V_{b} \\
\Delta V_{c}
\end{bmatrix}
= 0 \quad \text{(23)}$$

$$V_{l,k,h} = \frac{M_{1}}{2}V_{l,k} \angle \delta_{c,k}; \quad V'_{l,k,i} = \frac{M_{1}}{2}V_{l,k} \angle \delta_{c,k}';$$

$$Z_{k,h} = R_{k,h} + j\omega L_{k,h}; \quad Z'_{k,h} = R'_{k,h} + j\omega L'_{k,h}$$

(for $h = a,b,c$; $k = 1,2,3$; and $i = 1,2,3$)

According to (23), the phase a of injection side of DVR can be shown in Figure 3. In Figure 3, it can be seen that If the voltage magnitude in power system does not change, the capacitor voltage will remain constant. The operation of converters depend to mutually phases.

VI. DYNAMIC MODEL

Considering (19), the proposed dynamic model for DVR on dq0 axis is obtained as follows:

$$\begin{bmatrix}
\dot{x}_{dq0} \\
\dot{u}_{dq0}
\end{bmatrix} = \begin{bmatrix}
A_{dq0} & B_{dq0} \\
C_{dq0} & D_{dq0}
\end{bmatrix}
\begin{bmatrix}
x_{dq0} \\
u_{dq0}
\end{bmatrix} \quad \text{(24)}$$

where $x_{dq0}$ and $u_{dq0}$ are as follows and the matrices $A_{dq0}$ and $B_{dq0}$ have been explained in Appendix.

$$x_{dq0} = [i_{d,q} \quad i_{d,2,q} \quad i_{d,3,q} \quad i_{d,4,q} \quad v_{dc,1} \quad v_{dc,2} \quad v_{dc,3} \quad v_{dc,4}]^T \quad \text{(25)}$$

$$u_{dq0} = [i_{d,1,q} \quad i_{d,2,q} \quad i_{d,3,q} \quad i_{d,4,q} \quad v_{d,1} \quad v_{d,2} \quad v_{d,3} \quad v_{d,4}]^T \quad \text{(26)}$$

VII. SMALL SIGNAL DYNAMIC MODEL

The small signal model of DVR, (24) is linearized by making an approximation around the system operation point and is expressed as follows:

$$\Delta x_{dq0} = A'_{dq0}\Delta x_{dq0} + B'_{dq0}\Delta u_{dq0} \quad \text{(27)}$$

The non-zero arrays of $A'_{dq0}$ and $B'_{dq0}$ have been presented in appendix. The small signal model presented in (27) can be easily applied in a wide range of system dynamic investigations such as low frequency electromechanical modes.

VIII. SIMULATION RESULTS

In this section, the effects of variation of each DVR parameters are investigated and simulated in MATLAB software. Here, the DVR parameters are considered as Table 1. It is assumed that the system is symmetric.

A. Transient State Investigation

In this subsection, the dynamic stability of DVR is investigated through critical eigenvalues analysis. In order to select optimum signal, the transient behavior of input signals in converter 1 of phase ‘a’ are analyzed. According to simulation results that are shown in Figure 4, $\delta_1$, $\delta_2$, and $\delta_3$ do not improve the transient state stability parameters and do not affect on the dynamic stability of presented DVR. The $\delta_4$ improve the system’s critical eigenvalues and transient state stability parameters in converter 1 of phase ‘a’.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated voltage</td>
<td>240V ±1.03</td>
</tr>
<tr>
<td>Frequency</td>
<td>50Hz</td>
</tr>
<tr>
<td>Short circuit level</td>
<td>100MVA</td>
</tr>
<tr>
<td>Base voltage</td>
<td>240V</td>
</tr>
<tr>
<td>$X/R$</td>
<td>7</td>
</tr>
<tr>
<td>Nominal voltage</td>
<td>480kV</td>
</tr>
<tr>
<td>Frequency</td>
<td>50Hz</td>
</tr>
<tr>
<td>Active power</td>
<td>1000W</td>
</tr>
<tr>
<td>Reactive power</td>
<td>1000VAR</td>
</tr>
<tr>
<td>Resistance per unit length</td>
<td>0.01273Ω/km</td>
</tr>
<tr>
<td>Inductance per unit length</td>
<td>0.9337mH/km</td>
</tr>
<tr>
<td>Capacitance per unit length</td>
<td>12.74μF/km</td>
</tr>
<tr>
<td>Length</td>
<td>25km</td>
</tr>
</tbody>
</table>

B. Steady State Investigation

In order to study the steady state, the power flow of DVR is analyzed. The studied power system is shown in Figure 5. In this research, it is tried to increase the power flow with controlling parameters of DVR. Figure 6 shows the impact of DVR on the receiving-end active ($P_L$) and receiving-end reactive ($Q_L$) powers with and without DVR. According to simulation results shown in Figure 6, sending range of $P_L$ and $Q_L$ are improved using DVR.

IX. CONCLUSIONS

In this paper, a generalized mathematical model was presented for the presented DVR [4]. The dynamic model of steady and transient states were presented. The presented modeling can be applied in power flow calculations, system’s eigenvalues condition assess, and dynamic and transient state stability analysis. Steady and transient states, and small signal equations were derived. In additional, the effects of each DVR parameters...
variations were simulated. According to simulation results in steady state, DVR helps to improve active and reactive power flows in power system. In transient state evaluation, it can be mentioned that $\delta_i$ (for $i=a,b,c$) variations improve small signal stability in each converters.

APPENDIX

The non-zero arrays of matrix $A$, in (19) are:

\[ a_{e,e} = -\frac{R}{L} \quad \text{for} \quad e=1,2,3,4,5,6,7,8,9; \]

\[ a_{f,f} = -\frac{R'}{L'} \quad \text{for} \quad f=10,11,12; \]

\[ a_{1,13} = k_1 \cos \delta_1; \]

\[ a_{2,13} = k_1 \cos (\delta_1 - 120^\circ); \]

\[ a_{3,13} = k_1 \cos (\delta_1 - 240^\circ); \]

\[ a_{4,14} = k_2 \cos \delta_1; \]

\[ a_{5,14} = k_3 \cos (\delta_1 - 120^\circ); \]

\[ a_{6,14} = k_2 \cos (\delta_1 - 240^\circ); \]

\[ a_{7,15} = k_3 \cos (\delta_1); \]

\[ a_{8,15} = k_3 \cos (\delta_1 - 120^\circ); \]

\[ a_{9,15} = k_3 \cos (\delta_1 - 240^\circ); \]

\[ a_{10,13} = k_4 \cos \delta_1; \]

\[ a_{11,14} = k_2 \cos \delta_1; \]

\[ a_{12,15} = k_1 \cos \delta_1; \]

\[ a_{13,13} = k_5 \cos \delta_1; \]

\[ a_{13,2} = k_3 \cos (\delta_1 - 120^\circ); \]

\[ a_{13,2} = k_5 \cos (\delta_1 - 240^\circ); \]

\[ a_{14,4} = k_6 \cos \delta_1; \]

\[ a_{14,4} = k_6 \cos (\delta_1 - 120^\circ); \]

\[ a_{14,6} = k_6 \cos (\delta_1 - 240^\circ); \]

\[ a_{14,5} = k_7 \cos \delta_1; \]

\[ a_{15,8} = k_7 \cos (\delta_1 - 120^\circ); \]

\[ a_{15,9} = k_7 \cos (\delta_1 - 240^\circ); \]

\[ a_{13,10} = k_8 \cos \delta_1; \]

\[ a_{14,11} = k_9 \cos \delta_1; \]

\[ a_{15,12} = k_{10} \cos \delta_1. \]

The non-zero arrays of matrix $B$, in (19) are:

\[ b_{j,j} = \frac{1}{L} \quad \text{for} \quad j=1,2,3,4,5,6,7,8,9; \]

\[ b_{j,j} = \frac{1}{L'} \quad \text{for} \quad j=10,11,12. \]

The non-zero arrays of matrix $A_{dqo}$, in (24) are:

\[ a_{e,e} = -\frac{R}{L} \quad \text{for} \quad e=1,2,3,4,5,6; \]

\[ a_{f,f} = -\frac{R'}{L'} \quad \text{for} \quad f=7,8; \]

\[ a_{1,2} = a_{3,4} = a_{5,6} = a_{7,8} = -a_{2,1} = -a_{4,3} = -a_{6,5} = -a_{7,8} = 0; \]

\[ a_{1,9} = k_1 \cos \delta_1; \]

\[ a_{2,9} = k_1 \sin \delta_1; \]

\[ a_{3,10} = k_1 \cos \delta_1; \]

\[ a_{4,10} = k_2 \sin \delta_1; \]

\[ a_{5,11} = k_3 \cos \delta_1; \]

\[ a_{6,11} = k_3 \sin \delta_1; \]

\[ a_{7,11} = k_4 \cos \delta_1; \]

\[ a_{8,10} = k_4 \sin \delta_1; \]

\[ a_{9,10} = k_5 \cos \delta_1; \]

\[ a_{10,3} = 1.5k_5 \cos \delta_1; \]

\[ a_{11,6} = 1.5k_5 \cos \delta_1; \]

\[ a_{12,5} = 1.5k_5 \cos \delta_1; \]

\[ a_{13,8} = 1.5k_5 \sin \delta_1; \]

\[ a_{0,0} = 1.5k_5 \sin \delta_1. \]

The non-zero arrays of matrix $B_{dqo}$, in (24) are:

\[ b_{p,p} = \frac{1}{L} \quad \text{for} \quad p=1,2,3,4,5,6; \]

\[ b_{o,o} = \frac{1}{L'} \quad \text{for} \quad o=7,8. \]

The non-zero arrays of matrix $A'_{dqo}$, in (27) are:

\[ a_{m,m} = -\frac{R}{L'} \quad \text{for} \quad m=1,2,3,4,5,6; \]

\[ a_{n,n} = -\frac{R'}{L'} \quad \text{for} \quad n=7,8; \]

\[ a_{1,2} = a_{3,4} = a_{5,6} = a_{7,8} = -a_{2,1} = -a_{4,3} = -a_{6,5} = -a_{7,8} = 0; \]

\[ a_{9,1} = k_1 \cos \delta_1; \]

\[ a_{2,9} = k_1 \sin \delta_1; \]

\[ a_{3,10} = k_1 \cos \delta_1; \]

\[ a_{4,10} = k_2 \sin \delta_1; \]

\[ a_{5,11} = k_3 \cos \delta_1; \]

\[ a_{6,11} = k_3 \sin \delta_1; \]

\[ a_{7,11} = k_4 \cos \delta_1; \]

\[ a_{8,10} = k_4 \sin \delta_1; \]

\[ a_{9,10} = k_5 \cos \delta_1; \]

\[ a_{10,3} = 1.5k_3 \cos \delta_1; \]

\[ a_{11,6} = 1.5k_5 \cos \delta_1; \]

\[ a_{12,5} = 1.5k_5 \cos \delta_1; \]

\[ a_{13,8} = 1.5k_5 \sin \delta_1; \]

\[ a_{0,0} = 1.5k_5 \sin \delta_1. \]
The non-zero arrays of matrix $B'_{dep}$, in (27) are:

\[
\begin{align*}
&b_{11} = -k_1 v_{dc,1} \cos \delta_1; \quad b_{12} = k_1 m_1 \sin \delta_1; \\
&b_{3,5} = -k_1 v_{dc,2} \cos \delta_2; \quad b_{3,6} = k_1 m_2 \sin \delta_2; \\
&b_{4,0} = -k_1 v_{dc,3} \cos \delta_3; \quad b_{4,10} = k_1 m_3 \sin \delta_3; \\
&b_{2,1} = -k_1 v_{dc,1} \sin \delta_1; \quad b_{2,2} = -k_1 m_1 \cos \delta_1; \\
&b_{2,5} = -k_1 v_{dc,2} \sin \delta_2; \quad b_{2,6} = -k_1 m_2 \cos \delta_2; \\
&b_{4,1} = -k_1 v_{dc,3} \sin \delta_3; \quad b_{4,11} = -k_1 m_3 \sin \delta_3; \\
&b_{3,3} = k_2 v_{dc,1} \cos \delta_1; \quad b_{3,4} = k_2 m_1 \sin \delta_1; \\
&b_{3,7} = k_2 v_{dc,2} \cos \delta_2; \quad b_{3,8} = k_2 m_2 \sin \delta_2; \\
&b_{3,11} = k_2 v_{dc,3} \cos \delta_3; \quad b_{3,12} = k_2 m_3 \sin \delta_3; \\
&b_{4,3} = k_2 v_{dc,1} \sin \delta_1; \quad b_{4,4} = k_2 m_1 \sin \delta_1; \\
&b_{4,7} = k_2 v_{dc,2} \sin \delta_2; \quad b_{4,8} = k_2 m_2 \sin \delta_2; \\
&b_{4,11} = k_2 v_{dc,3} \sin \delta_3; \quad b_{4,12} = k_2 m_3 \sin \delta_3; \\
&b_{3,13} = k_3 (i_{dq} \cos \delta_1 + i_{d} \sin \delta_1); \\
&b_{3,2} = -k_3 m_1 (i_{dq} \sin \delta_1 - i_{d} \cos \delta_1); \\
&b_{3,3} = k_3 (i_{dq} \cos \delta_2 + i_{d} \sin \delta_2); \\
&b_{3,4} = k_3 m_1 (i_{dq} \sin \delta_2 - i_{d} \cos \delta_2); \\
&b_{3,5} = -k_3 m_2 (i_{dq} \sin \delta_3 - i_{d} \cos \delta_3); \\
&b_{3,6} = k_3 (i_{dq} \cos \delta_3 + i_{d} \sin \delta_3); \\
&b_{3,7} = -k_3 m_2 (i_{dq} \sin \delta_3 - i_{d} \cos \delta_3); \\
&b_{3,8} = k_3 m_3 (i_{dq} \sin \delta_2 - i_{d} \cos \delta_2); \\
&b_{3,9} = k_3 (i_{dq} \cos \delta_2 + i_{d} \sin \delta_2); \\
&b_{3,10} = -k_3 m_3 (i_{dq} \sin \delta_2 - i_{d} \cos \delta_2); \\
&b_{3,11} = -k_3 m_3 (i_{dq} \sin \delta_3 - i_{d} \cos \delta_3); \\
&b_{3,12} = k_3 m_3 (i_{dq} \sin \delta_3 - i_{d} \cos \delta_3)
\end{align*}
\]

**NOMENCLATURES**

- $i$: Index to show the phase of injected voltage
- $h$: Index to show the phase of voltage in converter $k$
- $L_k$ for $k = 1, 2, 3$: Filter inductance in converter $k$
- $C_k$ for $k = 1, 2, 3$: Filter capacitance in converter $k$
- $S_{b,h}$ for $i = a, b, c$: Bypass switch in phase $i$
- $R_k$ for $k = 1, 2, 3$: Resistance in converter $k$
- $v_{i,k,h}$ for $k = 1, 2, 3$, $h = a, b, c$: Input terminal voltage of phase $h$ in converter $k$
- $v_{i,m}$: Maximum value of input terminal voltage
- $v_{i,m,i}$ for $i = a, b, c$: Maximum value of output terminal voltage of phase $i$
- $\phi_i$ for $i = a, b, c$: Phase angle of injected voltage in phase $i$
- $v_{dc,k}$ for $k = 1, 2, 3$: Output voltage of filter capacitor in converter $k$
- $m_i$ for $i = a, b, c$: Transfer ratio of phase $i$
- $m_k$ for $k = 1, 2, 3$: Transfer ratio of converter $k$
- $S_{L,h}$ for $k = 1, 2, 3$, $h = a, b, c$: Switching function of positive half-cycle
- $S'_{L,h}$ for $k = 1, 2, 3$, $h = a, b, c$: Switching function of negative half-cycle
- $L_{h,k}$ for $k = 1, 2, 3$, $h = a, b, c$: Inductance in phase $h$ of converter $k$ in converter side
- $R_{h,k}$ for $k = 1, 2, 3$, $h = a, b, c$: Resistance in phase $h$ of converter $k$ in converter side
- $v_{h,k}$ for $k = 1, 2, 3$, $h = a, b, c$: Voltage of phase $h$ in converter $k$
- $v_{FH}$: Voltage drop in switch
- $v_{m,n}$: Middle point voltage of filter to null
- $r_i$: Switch resistance
- $R_{k,b}$ for $k = 1, 2, 3$, $h = a, b, c$: Equivalent resistance in phase $k$ of converter $h$ in converter side
- $d_{k,b}$ for $k = 1, 2, 3$, $h = a, b, c$: Switching function for positive half cycle of phase $h$ in converter $k$
- $d'_{k,b}$ for $k = 1, 2, 3$, $i = a, b, c$: Switching function for positive half cycle of phase $i$ in converter $k$
- $\delta_k$ for $k = 1, 2, 3$: Phase angle of converter $k$
- $\delta'_k$ for $i = a, b, c$: Phase angle of phase $i$
- $\Phi_k$ for $k = 1, 2, 3$: Voltage amplitude in converter $k$
- $\Phi'_k$ for $i = a, b, c$: Voltage amplitude in phase $i$
- $R'_{k,b}$ for $k = 1, 2, 3$, $i = a, b, c$: Equivalent resistance in phase $i$ of converter $h$ in injection side
- $v_{i,k,h}$ for $k = 1, 2, 3$, $i = a, b, c$: Voltage of phase $i$ in converter $k$
- $i_{d,k}$ for $k = 1, 2, 3$, $i = a, b, c$: Current of phase $i$ in converter $k$
- $i_{dc,k}$ for $k = 1, 2, 3$: DC current in converter $k$
- $i_{dc,k}$ for $k = 1, 2, 3$: DC current passing through converter $k$ in converter side
- $i'_{dc,k}$ for $k = 1, 2, 3$: DC current passing through converter $k$ in injection side
- $R$: Equivalent resistance in converter side
- $R'$: Equivalent resistance in injection side
- $L$: Equivalent inductance in converter side
- $L'$: Equivalent inductance in injection side
- $A$: State matrix
- $B$: Input matrix
- $X$: State vector
- $U$: Input vector
$L_{t,h}$: Inductance of transformer in phase $h$

$i_{k,h}$: Current of phase $h$ in converter $k$

$I_{k,i}^*$ for $k = 1, 2, 3, i = a, b, c$: Inductance in phase $i$ of converter $k$ in injection side

$r_{h,h}$ for $h = a, b, c$: Resistance of transformer in phase $h$

$V_{i,h,k}$ for $k = 1, 2, 3, h = a, b, c$: Steady state input terminal voltage of phase $h$ in converter $k$

$X_{i,h,k}$ for $k = 1, 2, 3, h = a, b, c$: Reactance of phase $h$ in converter $k$

$I_{k,h}$ for $k = 1, 2, 3, h = a, b, c$: Steady state current of phase $h$ in converter $k$

$V_{i,h,k}$ for $k = 1, 2, 3, h = a, b, c$: Steady state voltage of phase $h$ in converter $k$

$V_{i,k}^*$ for $k = 1, 2, 3, i = a, b, c$: Steady state output terminal voltage of phase $i$ in converter $k$

$X_{i,k}^*$ for $k = 1, 2, 3, i = a, b, c$: Reactance of phase $i$ in converter $k$

$I_{k,i}^*$ for $k = 1, 2, 3, i = a, b, c$: Steady state current of phase $i$ in converter $k$

$V_{i,k}^*$ for $k = 1, 2, 3, i = a, b, c$: Steady state voltage of phase $i$ in converter $k$

REFERENCES


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